

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 251023US2S		SERIAL NO. 10/807,454		
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Yoshihisa IWATA et al.						
		ALLOWED DATE December 6, 2005		FILING DATE March 24, 2004		GROUP 2827		
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT & TRADEMARK NUMBER	DATE	NAME		CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY		TRANSLATION		
TP	AO	2003-173672	06/20/2003	JAPAN		YES	NO X	
TP	AP	2003-257175	09/12/2003	JAPAN			X	
	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
TP	AW	Takeshi HONDA et al., "MRAM-Writing Circuitry to Compensate for Thermal-Variation of Magnetization-Reversal Current", 2002 Symposium On VLSI Circuits Digest of Technical Papers, IEEE, June 2002, pp. 156-157.						
	AX							
	AY							
	AZ					<input type="checkbox"/> Additional References sheet(s) attached		
Examiner Trong Phan		Date Considered 1/31/06						
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								